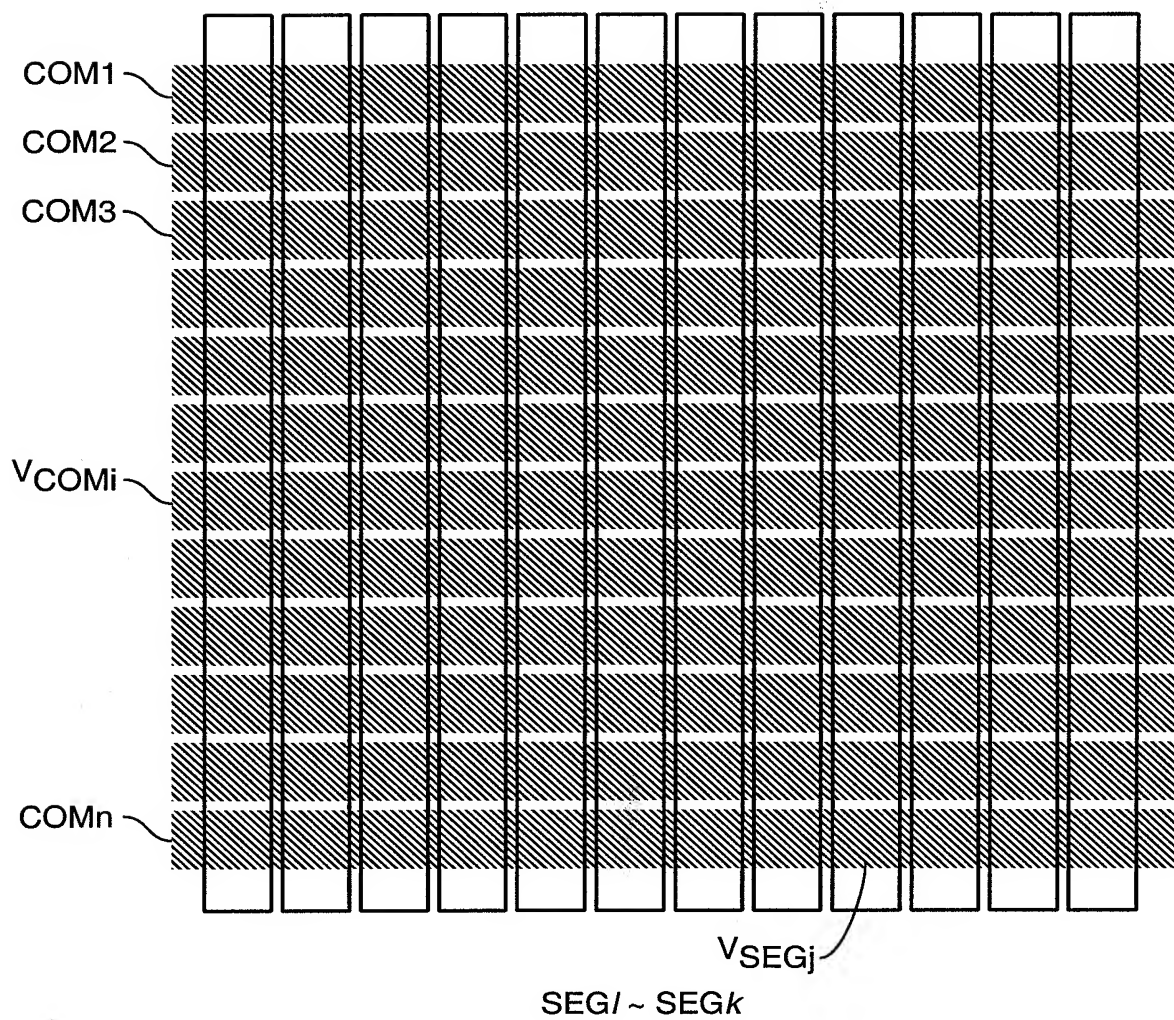
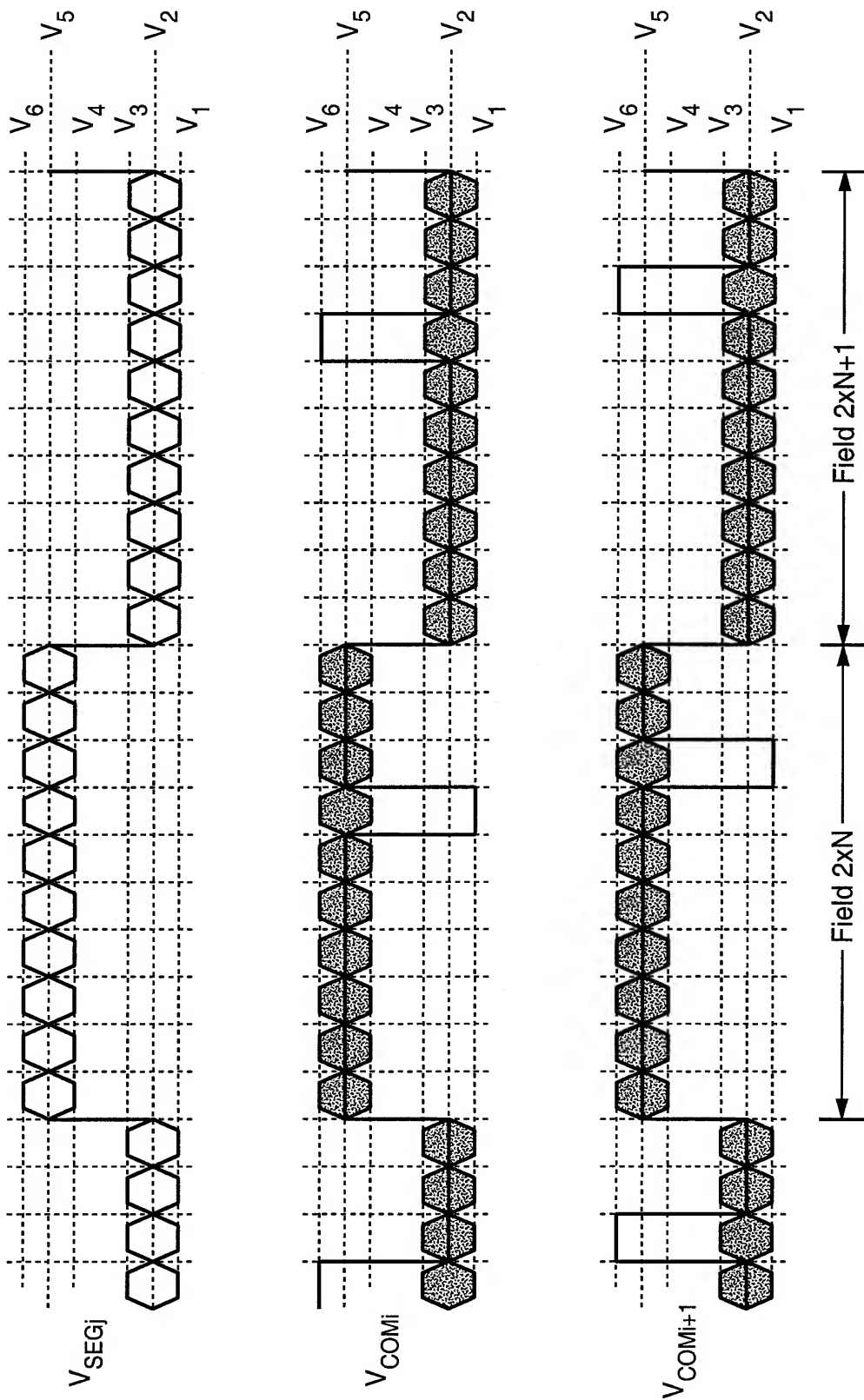


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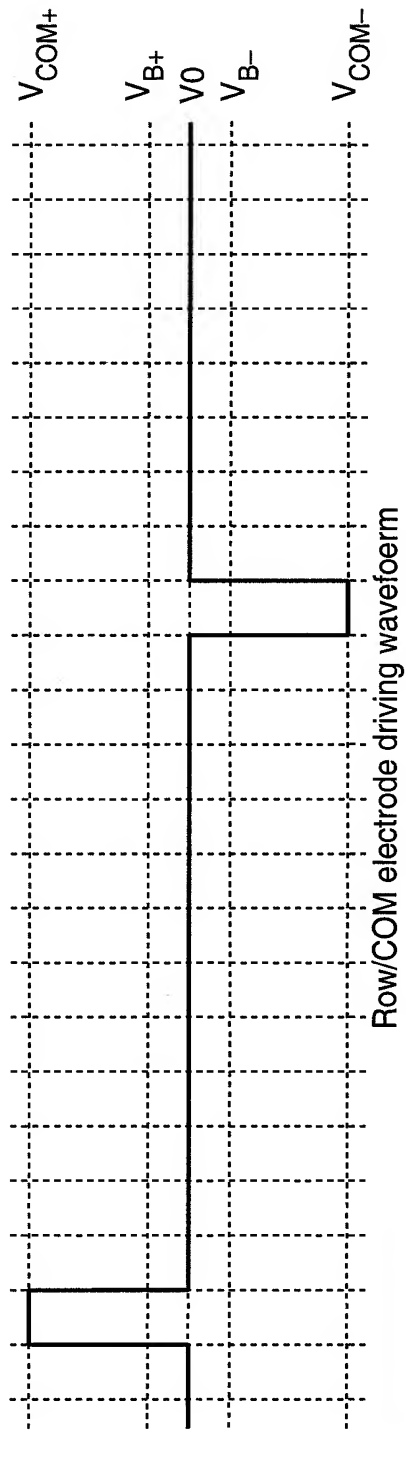
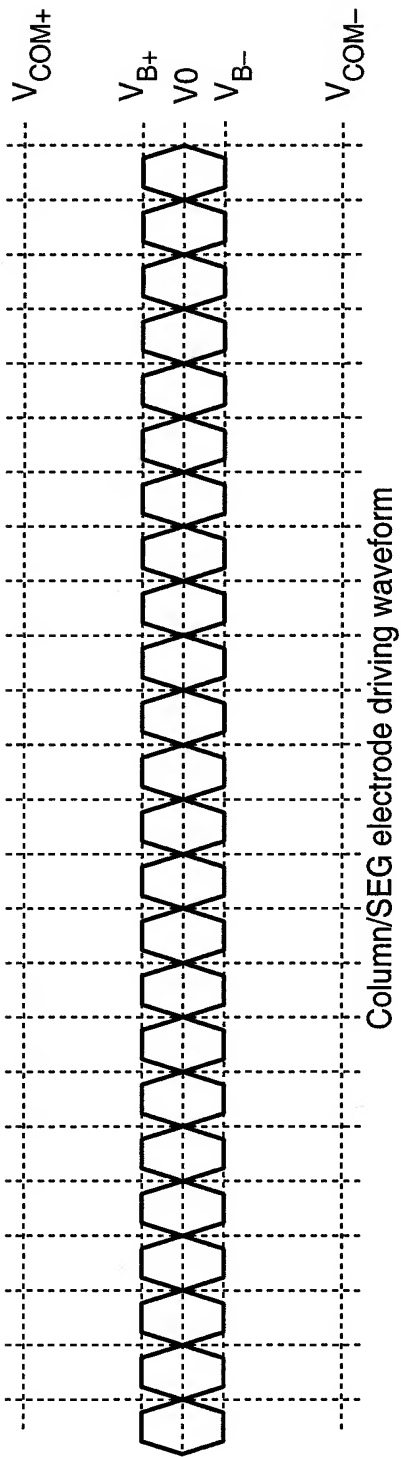
**FIG. 1**

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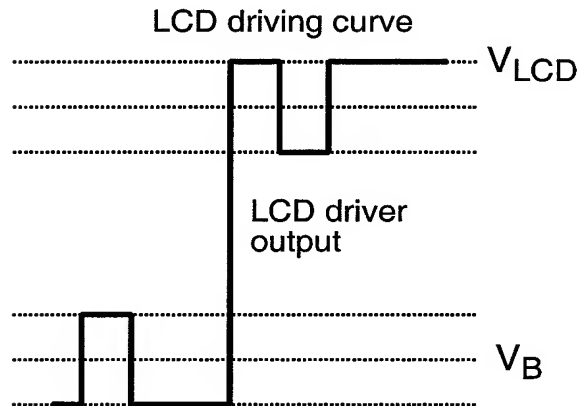
**FIG. 2a**  
 (PRIOR ART)

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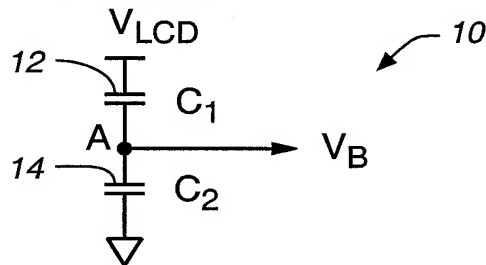
**FIG. 2b**  
 (PRIOR ART)

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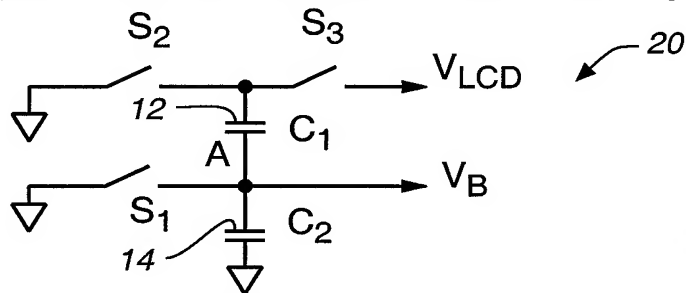
**FIG.\_2c**

A capacitor bias ratio generation circuit



**FIG.\_3**

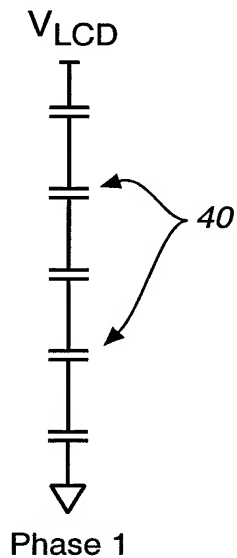
A capacitor bias ratio circuit with periodic refreshing



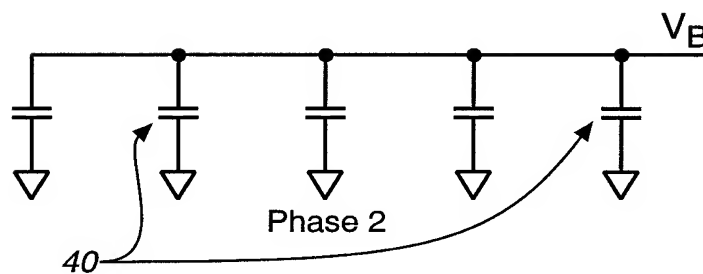
**FIG.\_4**

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An alternative capacitor divider bias ratio generation circuit



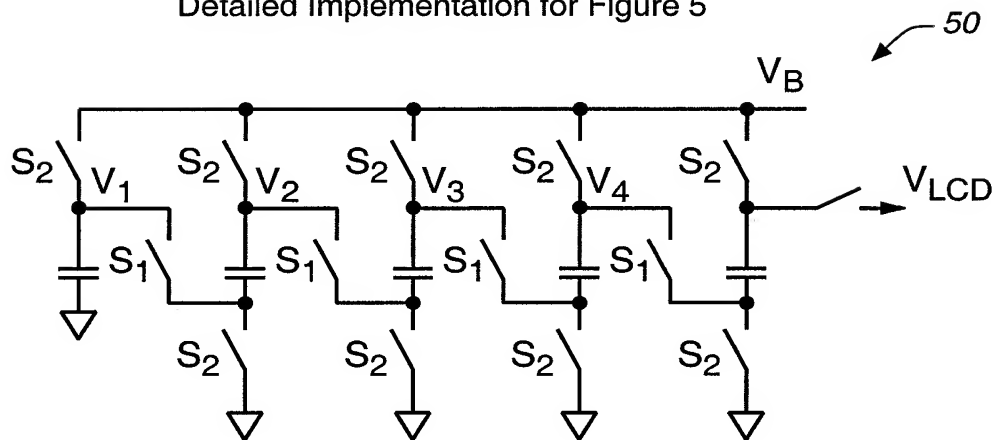
**FIG. 5a**



**FIG. 5b**

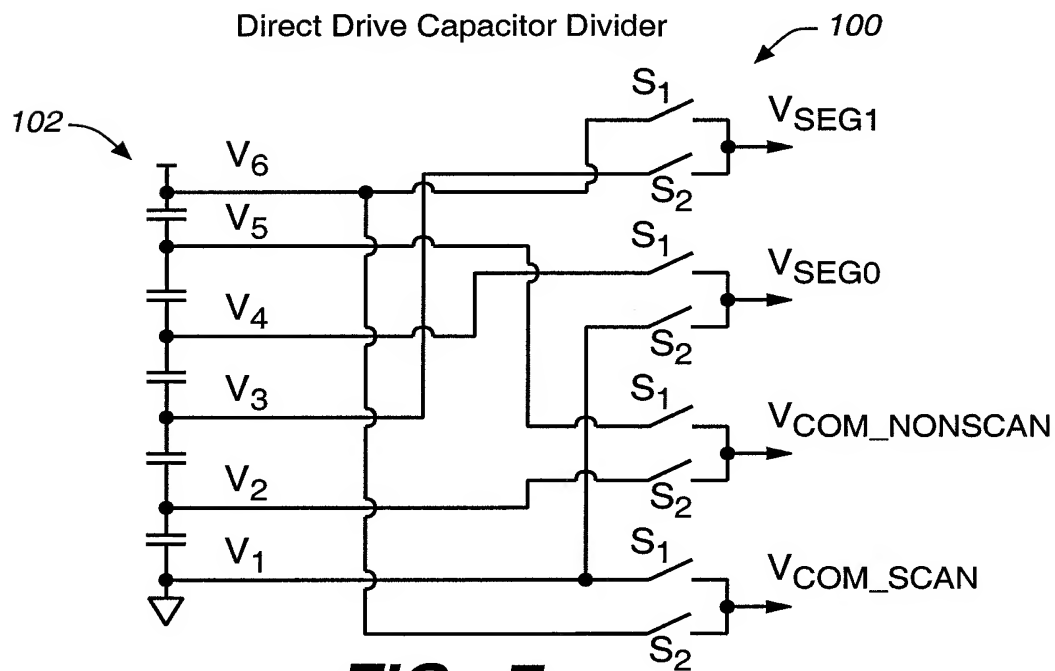
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Detailed Implementation for Figure 5



**FIG.\_6**

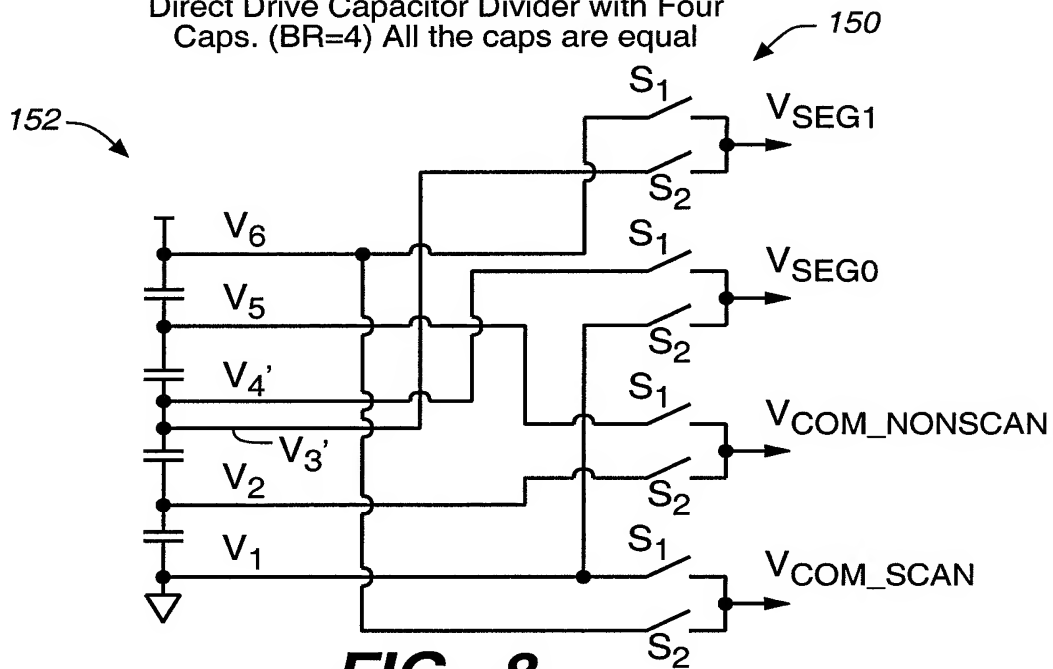
Direct Drive Capacitor Divider



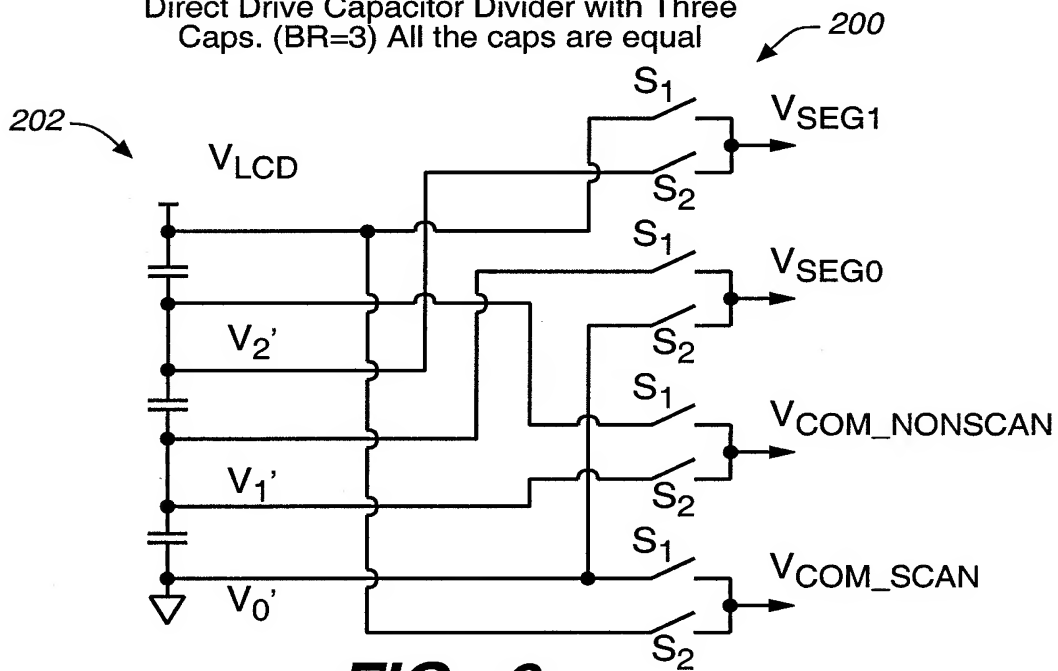
**FIG.\_7**

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Direct Drive Capacitor Divider with Four Caps. (BR=4) All the caps are equal



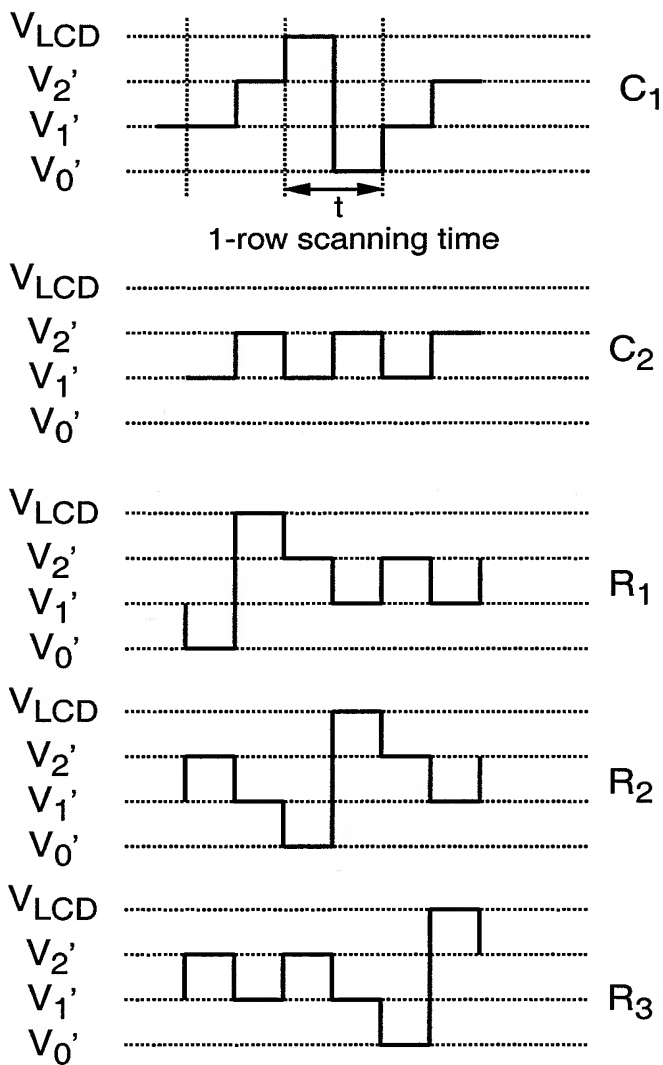
Direct Drive Capacitor Divider with Three Caps. (BR=3) All the caps are equal



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1/3 LCD driving waveform

	C1	C2
R1		
R2		
R3		



**FIG.\_10**